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PATENT APPLICATION**THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q80572

Mitsuhiko SHIMAMOTO

Allowed: July 18, 2005

Appln. No.: 10/803,983

Group Art Unit: 2816

Confirmation No.: 6913

Examiner: Minh T. NGUYEN

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U.S. Patent No.: 6,972,609

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

SUBMISSION OF ART

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

For the possible benefit of anyone subsequently evaluating the scope and/or validity of the above-identified patent, it is requested that the documents that are listed below (copies enclosed, except for the U.S. patent publications) be placed in the U.S. Patent and Trademark Office's file wrapper of the above-identified U.S. patent:

1. United States Patent Application Publication No. 2001/0038637, published November 8, 2001.
2. United States Patent Application Publication No. 2002/0131596, published September 19, 2002.
3. German Patent Application Publication No. 198 28 936, published December 2, 1999.
4. European Patent Application Publication No. 1 085 393, published March 21, 2001.

The above-listed documents were recently cited in a European Search Report, dated October 19, 2005, a copy of which is enclosed.

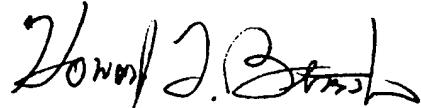
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The undersigned has not reviewed the teachings of the above-listed documents in detail and thus makes no representations concerning the relevancy or materiality of the above-listed documents.

This is not an Information Disclosure Statement and no response from the U.S. Patent and Trademark Office is believed to be necessary, nor are any fees believed to be due.

Respectfully submitted,



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